This year’s proud University Booth sponsors are:
MONDAY JUNE 9TH - THURSDAY, JUNE 12TH 2008

9 - 10:00 AM
SOC / Platform Based Design
Advisor: Rainer Leupers
RWTH Aachen University
Demo 1

SOC / Platform Based Design
Advisor: Rainer Leupers
RWTH Aachen University
Demo 6

Deep Submicron
Advisor: Yih-Lang Li
National Chiao Tung University
Demo 18

Embedded Systems
Advisor: Donald Chiarulli
University of Pittsburgh
Demo 43

Synthesis
Advisor: Hossein Pedram
Amirkabir University of Technology
Demo 3

FPGA / Reconfigurable
Advisor: Elaheh Bozorgzadeh
University of California, Irvine
Demo 22

10 - 11:00 AM
SOC / Platform Based Design
Advisor: Rainer Leupers
RWTH Aachen University
Demo 5

SOC / Platform Based Design
Advisor: Sandro Rigo
Unicamp
Demo 36

Deep Submicron
Advisor: Yih-Lang Li
National Chiao Tung University
Demo 19

Embedded Systems
Advisor: Patrick Schaumont
Virginia Tech
Demo 13

Synthesis
Advisor: Ching-Yang (Ric) Huang
National Taiwan University
Demo 11

FPGA / Reconfigurable
Advisor: Coussy Philippe
European University of Brittany - UBS
Demo 4

11 - 12:00 PM
SOC / Platform Based Design
Advisor: Jim Plusquellic
University of Maryland Baltimore County
Demo 6

SOC / Platform Based Design
Advisor: Chi-Heng Wen
National Chung Cheng University
Demo 29

Deep Submicron
Advisor: Yih-Lang Li
National Chiao Tung University
Demo 21

Embedded Systems
Advisor: Soonhoi Ha
Seoul National University
Demo 15

Synthesis
Advisor: Adriel M. Ziesemer Jr.
UFRGS
Demo 32

FPGA / Reconfigurable
Advisor: Seen Wook Kim
Korea University
Demo 9

12- 1:00 PM
SOC / Platform Based Design
Advisor: Sander Stuijk
Eindhoven University of Technology
Demo 7

Custom Architecture
Advisor: Liang-Gee Chen
National Taiwan University
Demo 2

Deep Submicron
Advisor: Yao-Wen Chang
National Taiwan University
Demo 23

Embedded Systems
Advisor: Pushkar Mishra
University of Florida
Demo 8

Synthesis
Advisor: Ricardo Reis
UFRGS
Demo 34

FPGA / Reconfigurable
Advisor: Ili Vasilkin
University of Maryland
Demo 14

1 - 2:00 PM
SOC / Platform Based Design
Advisor: Cheng-Wen Wu
National Tsing Hua University
Demo 17

Custom Architecture
Advisor: Hyunchul Shin
Hanyang University
Demo 26

Deep Submicron
Advisor: Shawn Blanton
Carnegie Mellon University
Demo 29

Embedded Systems
Advisor: Jihong Kim
Seoul National University
Demo 37

Low Power
Advisor: Yung-Hsiang Lu
Purdue University
Demo 12

FPGA / Reconfigurable
Advisor: Karel Bruneel
Ghent University
Demo 31

2 - 3:00 PM
SOC / Platform Based Design
Advisor: Kyoung Choi
Seoul National University
Demo 27

Custom Architecture
Advisor: Baris Taskin
Drexel University
Demo 41

Deep Submicron
Advisor: Yih-Lang Li
National Chiao Tung University
Demo 35

Low Power
Advisor: Naehyuck Chang
Seoul National University
Demo 25

Veriﬁcation
Advisor: Masahiro Fujita
University of Tokyo
Demo 20

3 - 4:00 PM
SOC / Platform Based Design
Advisor: Diana Marqueses
Carnegie Mellon University
Demo 28

Analog Design
Advisor: Jorge Fernandez Villena
INESC-ID / IST, TU Lisbon
Demo 24

Deep Submicron
Advisor: Yih-Lang Li
National Chiao Tung University
Demo 41

Low Power
Advisor: Shih-Hsiang Lu and Mely Chen Chi
Chung Yuan Christian University
Demo 16

FPGA / Reconfigurable
Advisor: Alex Jones
University of Pittsburgh
Demo 42

Collaboration
Advisor: David Money Harris
Harvey Mudd College
Demo 40

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